

FIG. 1

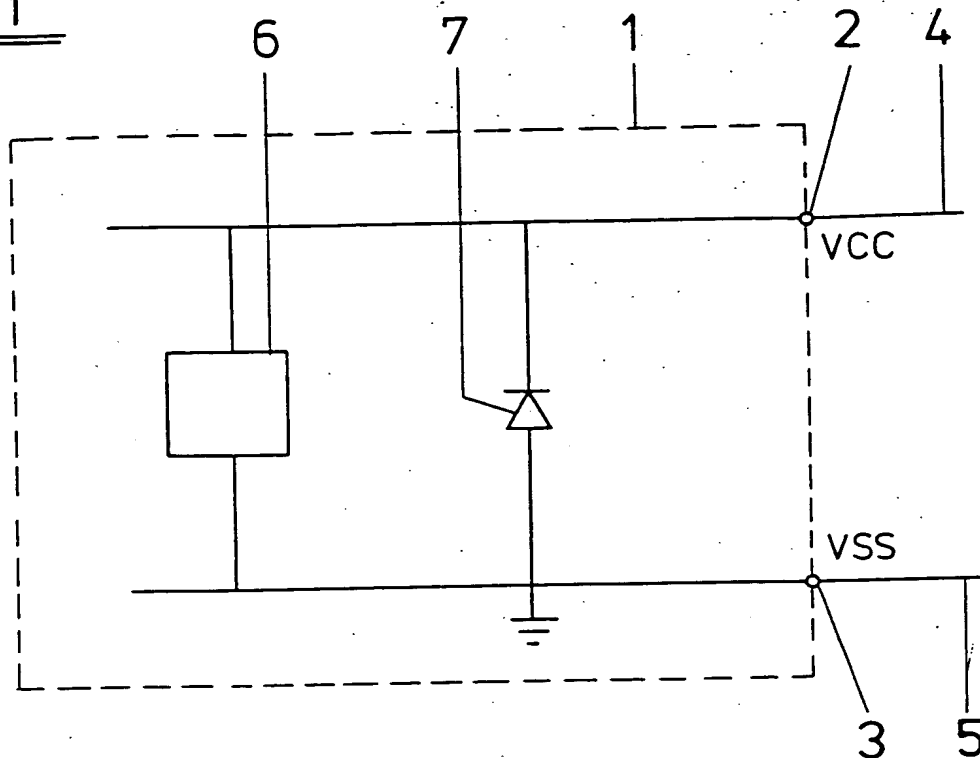
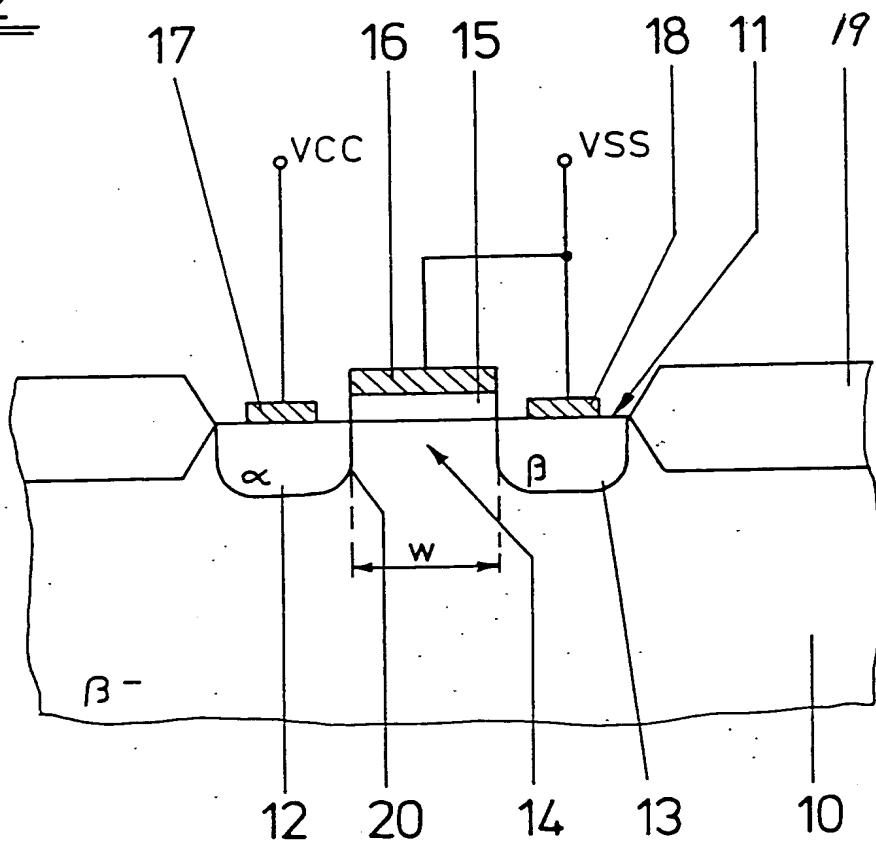


FIG. 2



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A cross-sectional diagram of a semiconductor device, likely a CMOS or BiCMOS process, showing three transistors labeled  $\beta$ ,  $\alpha$ , and  $\beta$ . The substrate is labeled  $\beta^-$ . The top layer is labeled 18. Below it are layers 15, 16, 17, 16, 15, 18, 11, and 19. The gates are connected to a common line that branches to VSS and VCC. Contacts are shown at the bottom of each transistor. Arrows point to the base regions of the  $\beta$  and  $\alpha$  transistors.